AN 210503

ESD Protection for IoT FEM with Chip Varistor

Application Notes

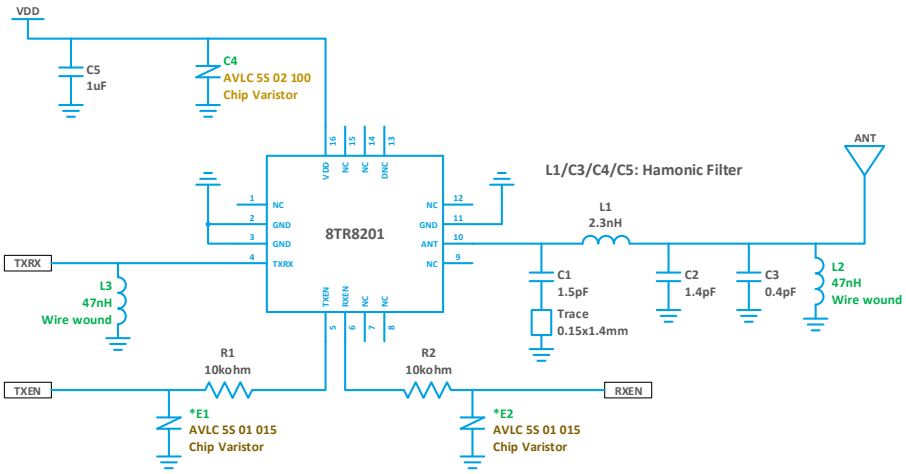
This application note explains how additional devices circuit delivers protection levels of ±4KV ESD HBM(Human Body Model). It will help in the long-term reliability of the final product.
For more detailed product specifications of the 8TR82xx, please check the datasheet.

Additional devices

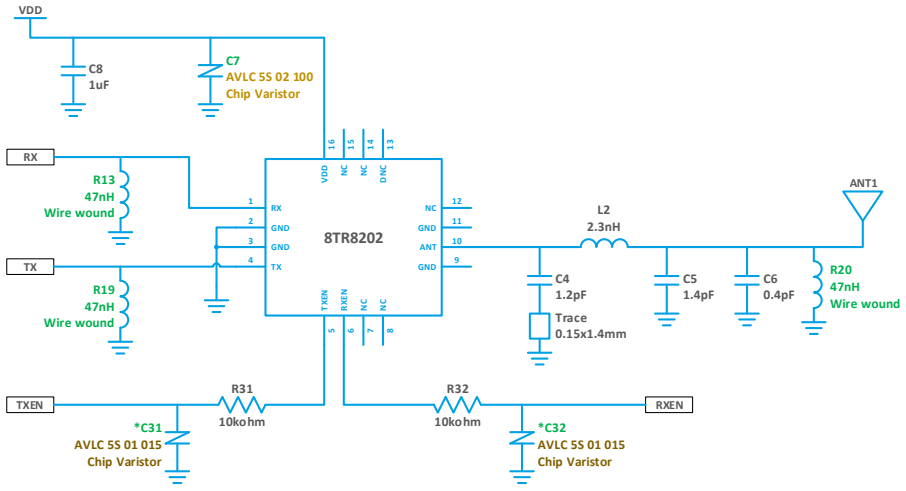
The protection devices added in the circuit are:
- RF Port(ANT, TXRX): 47nH Wire wound Inductor (Murata)
- VDD Line: AVLC_5S_02_100 Chip Varistor (AMOTECH)
- Control Line(TXEN, RXEN): AVLC_5S_01_015 Chip Varistor (AMOTECH)

The level of protection provided has little effect due to part placement and orientation. Depending on the user's needs, it can be placed near the connector port or any device that needs protection.

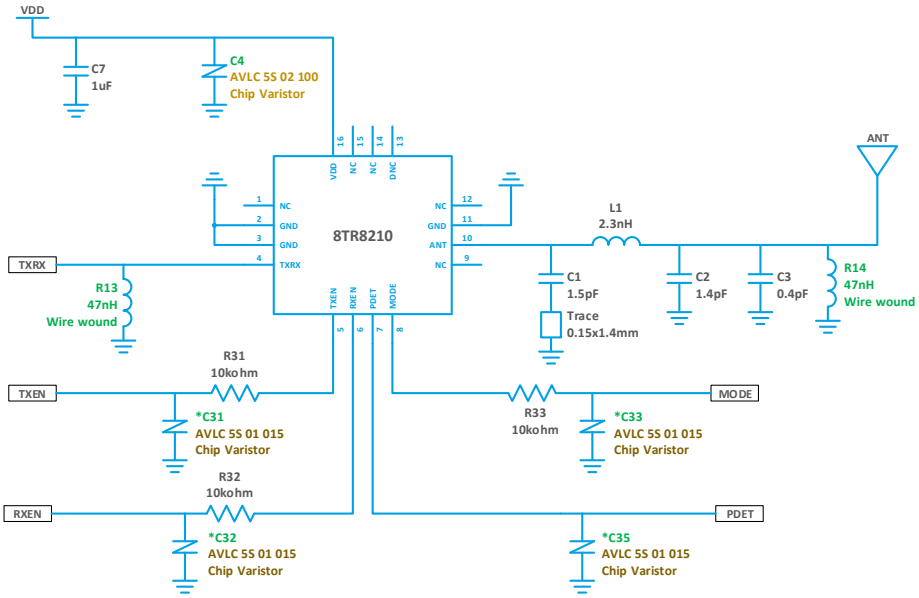
Application Schematic - 8TR8201



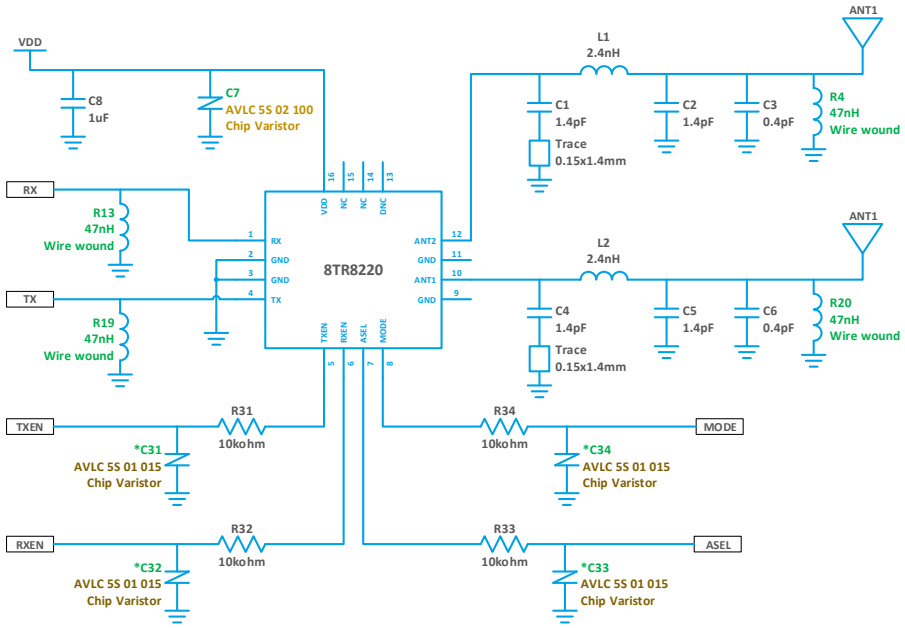
Application Schematic - 8TR8202



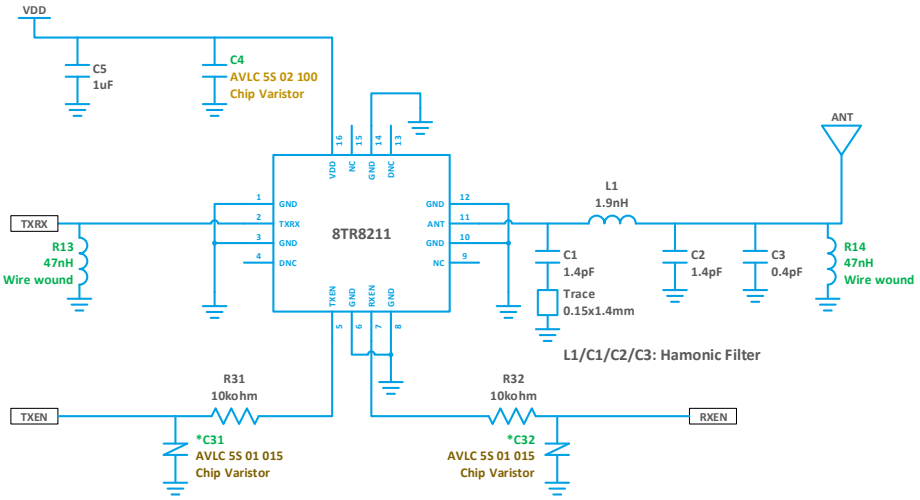
Application Schematic - 8TR8210



Application Schematic - 8TR8220



Application Schematic - 8TR8211 & 8TR8211C



Results & Recommendation

The additional device circuit was tested normally on the ±4KV ESD HBM(Human Body Model), after which it was confirmed that the parts continued to operate without any problems.

For long-term reliability of the final product, it is recommended to use the mentioned device (or the same specification). It will be able to achieve the required effect with minimal space.

*Chip Varistor Electrical characteristics

Part No.	Vdc(1)	Varistor voltage (Vn) @1mA DC	Leakage Current (IL) @Vdc	Cp (@ 1kHz, Vrms=0.5V)	Clamping Voltage (VC)	Peak Current (Imax)	Insulation Resistance (IR) @3.6V
	[V]	[V]	[uA]	[pF]	[V]	[A]	[MΩ]
AVLC_5S_01_015	5.5	10-15.6	50	15 (10.5-19.5)	Max. 35	Max. 1	Min. 10
AVLC_5S_02_100	5.5	10-14	Max. 20	100 (70-130)	25	20	Min. 10

(1) Maximum continuous DC working voltage